

033082M298

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Tsuneyuki OKABE, et al.

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U.S. Serial No.: To Be Assigned

Group Art Unit: To Be Assigned

Filed: : Herewith

Examiner: To Be Assigned

For: SEMICONDUCTOR MANUFACTURING APPARATUS AND  
SEMICONDUCTOR MANUFACTURING METHOD

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Pursuant to the duty of disclosure under 37 C.F.R. 1.56, Applicants are enclosing an Information Disclosure Citation Form (PTO-1449) which lists all of the documents noted in the International Search Report filed herewith. Copies of the cited documents are enclosed.

It is respectfully requested that the cited documents be considered by the Examiner in the above-identified patent application and that the cited documents be made officially of record therein. It is further requested that a listing of the same appear on the face of any patent which may issue from this application.

Respectfully submitted,  
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	APPLICANT: Tsuneyuki OKABE, et al.	
	FILING DATE Herewith	GROUP ART UNIT To Be Assigned

U.S. PATENT DOCUMENTS

*Examiner's Initials		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE, IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						

FOREIGN PATENT DOCUMENTS

*Examiner's Initials		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION YES NO	
	AH	2000-122725	4/28/00	Japan			Abstract	
	AI	09-016268	1/17/97	Japan			Abstract	
	AJ	07-281760	10/27/95	Japan			Abstract	
	AK							
	AL							
	AM							
	AN							
	AO							
	AP							

OTHER INFORMATION (Including Author, Title, Date, Pertinent Pages, Etc.)

	AQ	
	AR	
	AS	
EXAMINER:		DATE CONSIDERED:
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>		